



TOP VIEW THROUGH THE PRODUCT

INPUT	
PAD #	FUNCTION
1A1	+IN
1A2	PWM_Y
1A3	VDD
1A4	PWM_X
1A5	+IN
1B1	-IN
1B5	-IN

OUTPUT	
PAD #	FUNCTION
2A1	START
2A5	PWM_S
2B1	TMN
2B5	VCC
2C1	TMP
2C5	CSP
2D1	GND
2D2	VOUT
2D3	CSN
2D4	VOUT
2D5	GND

LAND GRID ARRAY PAD DESIGNATION															
	A	B	C	D	E	F	G	H	J	K	L	M	N	P	R
1	N/A	N/A	N/A	PFAULT_IN#	PUCDTO	PUCDTI	PWM1Y	PWM2Y	PWM3Y	PWM4Y	PWM5Y	N/A	N/A	N/A	N/A
2	N/A	N/A	N/A	VSRMON	PUCCS	PUCCK	PWM1X	PWM2X	PWM3X	PWM4X	PWM5X	N/A	N/A	N/A	N/A
3	-IN	-IN	N/A	TMP5	CSP5	CSN5	GND	GND	GND	GND	PWM6X	PWM6Y	START5	GND	GND
4	-IN	-IN	N/A	TMP3	CSP3	CSN3	GND	GND	GND	GND	FAULT#	START6	START3	GND	GND
5	-IN	-IN	N/A	TMP2	CSP2	CSN2	GND	GND	GND	GND	VR_RDY	VREG	START2	GND	GND
6	-IN	-IN	N/A	TMP4	CSP4	CSN4	GND	GND	GND	GND	EN	VCTRL	START4	GND	GND
7	N/A	N/A	N/A	TMP6	CSP6	CSN6	SALERT	SDA	SVDAT / AVSMDAT	VR_HOT#	VCCIO_OK	N/A	N/A	N/A	N/A
8	N/A	N/A	N/A	TMN	+S	-S	SADDR	SCL	SVCLK / AVSCLK	SV_ALERT / AVSSDAT	PAD_ALERT#	N/A	N/A	N/A	N/A

NOTES:

1. LAND DESIGNATOR PER JEP95, SEC. 3, SPP-010 AND SPP-020
2. ZERO ORIENTATION WITH PIN 1 IN LOWER LEFT CORNER
3. DETAILS OF PIN 1 IDENTIFIER ARE OPTIONAL, BUT MUST BE LOCATED WITHIN THE ZONE INDICATED. THE PIN IDENTIFIER MAY BE EITHER A MARKED FEATURE OR A CHAMFERED EDGE

	Title	PSA M1 30 X 12.7mm MAIN Module	Designed	RG / GM	01 / 12 / 17
	Description	Package Pin-Out	Checked	G. Mauri	06 / 12 / 17
	Engineering change order :		Approved	MA / AB	
			Revised	G. Mauri	12 / 12 / 17
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